

HALAMAN PENGESAHAN KTI

PENILAIAN FAKTOR-FAKTOR YANG TERKANDUNG DALAM VISUM ET REPERTUM OLEH DOKTER UGD DI RUMAH SAKIT PKU MUHAMMADIYAH YOGYAKARTA TAHUN 2009

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1.2.2.2. *Stability of the system*

For the system to be stable, the condition $\left| \frac{d\theta}{dt} \right| < \epsilon$ must be met. This condition can be met by decreasing the value of α . As α decreases, the value of $\frac{d\theta}{dt}$ also decreases.

1.2.2.3. *Conclusion*

The system is stable if $\left| \frac{d\theta}{dt} \right| < \epsilon$.

1.2.3. *Implementation of the system*

1.2.3.1. *Block diagram*

The block diagram of the system is shown below. The input signal is θ_{in} , which is fed into the summing junction. The output of the summing junction is fed into the integrator block. The output of the integrator block is θ , which is fed into the actuator block. The actuator block outputs a signal θ_{out} , which is fed back into the summing junction.

1.2.3.2. *Block diagram*

The block diagram of the system is shown below. The input signal is θ_{in} , which is fed into the summing junction. The output of the summing junction is fed into the integrator block. The output of the integrator block is θ , which is fed into the actuator block. The actuator block outputs a signal θ_{out} , which is fed back into the summing junction.

1.2.3.3. *Block diagram*

The block diagram of the system is shown below. The input signal is θ_{in} , which is fed into the summing junction. The output of the summing junction is fed into the integrator block. The output of the integrator block is θ , which is fed into the actuator block. The actuator block outputs a signal θ_{out} , which is fed back into the summing junction.

1.2.3.4. *Block diagram*

The block diagram of the system is shown below. The input signal is θ_{in} , which is fed into the summing junction. The output of the summing junction is fed into the integrator block. The output of the integrator block is θ , which is fed into the actuator block. The actuator block outputs a signal θ_{out} , which is fed back into the summing junction.

1.2.3.5. *Block diagram*

The block diagram of the system is shown below. The input signal is θ_{in} , which is fed into the summing junction. The output of the summing junction is fed into the integrator block. The output of the integrator block is θ , which is fed into the actuator block. The actuator block outputs a signal θ_{out} , which is fed back into the summing junction.

1.2.3.6. *Block diagram*

The block diagram of the system is shown below. The input signal is θ_{in} , which is fed into the summing junction. The output of the summing junction is fed into the integrator block. The output of the integrator block is θ , which is fed into the actuator block. The actuator block outputs a signal θ_{out} , which is fed back into the summing junction.